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PATENT
10/31/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Kinsman et al.

Serial No.: 09/917,127

Filed: July 27, 2001

For: METHODS FOR FABRICATING A
CHIP SCALE PACKAGE USING WAFER
LEVEL PROCESSING AND DEVICES
RESULTING THEREFROM

Confirmation No.: 3326

Examiner: M. Trinh

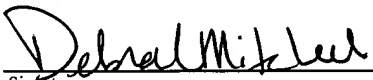
Group Art Unit: 2822

Attorney Docket No.: 3572.1US (97-1243.1)

CERTIFICATE OF MAILING

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service as First Class Mail on the date of deposit shown below with sufficient postage and in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231.

October 18, 2002
Date


Signature

Debra L. Mitchell
Name (Type/Print)

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the documents listed on attached Form PTO-1449 or PTO/SB/08 be considered by the Examiner and made of record. Copies of the listed documents are enclosed pursuant to 37 C.F.R. § 1.98(a). The listed documents were cited by the Office in co-pending application Serial No. 09/586,243, filed on June 2, 2000, and directed to a related invention.

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an admission that the information cited herein is, or is considered to be, material to patentability as

defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicants herein that no other possible material information as defined in 37 C.F.R. § 1.56(b) exists.

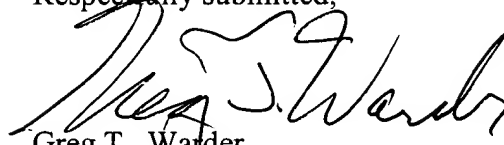
<u>U.S. Patent Documents</u>		
<u>U.S. Patent No.</u>	<u>Publication Date</u>	<u>Patentee</u>
US - 5,834,843	11/1998	Mori et al.
US - 5,977,641	11/1999	Takahashi et al.
US - 6,181,569	01/2001	Chakravorty
US - 6,297,553	10/2001	Horiuchi et al.
US - 6,379,999	04/2002	Tanabe

Applicants offer to supply any explanation or discussion of the documents that the Examiner feels is necessary or desirable and which is requested.

This Supplemental Information Disclosure Statement is filed after the mailing date of the first Office Action on the merits. I hereby certify that no item of information contained in the Supplemental Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of the statement, and therefore no fee is due.

Serial No. 09/917,127

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Greg T. Warder", is written over the typed name.

Greg T. Warder

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Date: October 18, 2002

GTW/ps:dlm

Enclosures: Form PTO-1449 or PTO/SB/08

Cited Documents

Document in ProLaw